

CHIPFIXMASTER INSTITUTE

MacBook REPAIRING COURSE

Dwarka Mor Metro Station Gate no.2 Building no. B-2 Sewak Park, New Delhi -110059

Mobile: 8920778160, 9718611260 Email: Chipfixmaster@gmail.com

SYLLABUS

Module-1

- **About Current (i), About Voltage (v)**
- **Electrical & Electronics**
- **Ohm's Law, Power (Watt)**
- **Resistor**
 - Resistance
 - Combination of Resistors
 - Resistor as a Voltage Divider
 - Unit of Resistance
 - Working
 - Testing
- **Capacitor**
 - Working
 - Testing
 - R.C. Time Delay Circuit
- **Coil (L)**
 - Working
 - Checking
- **Fuse**
 - Working
 - Testing
- **Conductor, Insulator, Semiconductor (N & P Type)**
- **Diode**



- Combination of Diodes
- Working
- Testing

- **Transistor**

- Working
- Testing

- **Mosfet**

- Types of Mosfets
- Working
- Testing

- **Power IC, Digital IC**

- Working
- Pin Details
- Testing

- **Regulator**

- About Regulators
- Step Down Regulator
- Single Phase & Dual Phase Regulator
- Regulator with Mosfet & Without Mosfet
- Testing

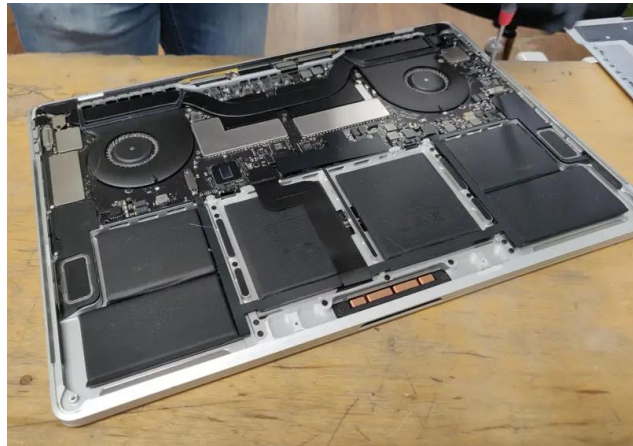
- **Logic Gates**

- **Crystal**

- Working

- **Use of Tools**

- Multi-meter
- Temperature Controlled Iron
- Temperature Controlled SMD
- D.C. Power Supply
- VGA Machine
- D.S.O. (Digital Storage Oscilloscope)



Module-2

- **S3 State Powers Formation**

- +1.8V
- +1.0V
- +1.05V
- +VCCSA

- **VGA Powers**

- VGA_Core
- +1.0VPCI_VGA
- +Ram_Run_VGA
- +3.3V_Run_VGA

- **Formation of Core Enable Signal**

- VR_ON

- **CPU Core Regulator**

- **Clock Generator IC**

- **CPU GFX Regulator**

- **Basic Power Requirement**

- ICH/PCH
- Graphic
- CPU
- SOC

- **Connectors Pin Details**

- **Schematics Reading**

- Block Diagram
- Power Plane
- Power Rail
- Power Tree
- Timing Sequence

- **Circuit Tracing Using Schematics**

- **BIOS ME Region Cleaning**

- LBE Software

- **About Board Viewer**

- **Complete Troubleshooting of Motherboard**

